

FIG. 1 PRIOR ART

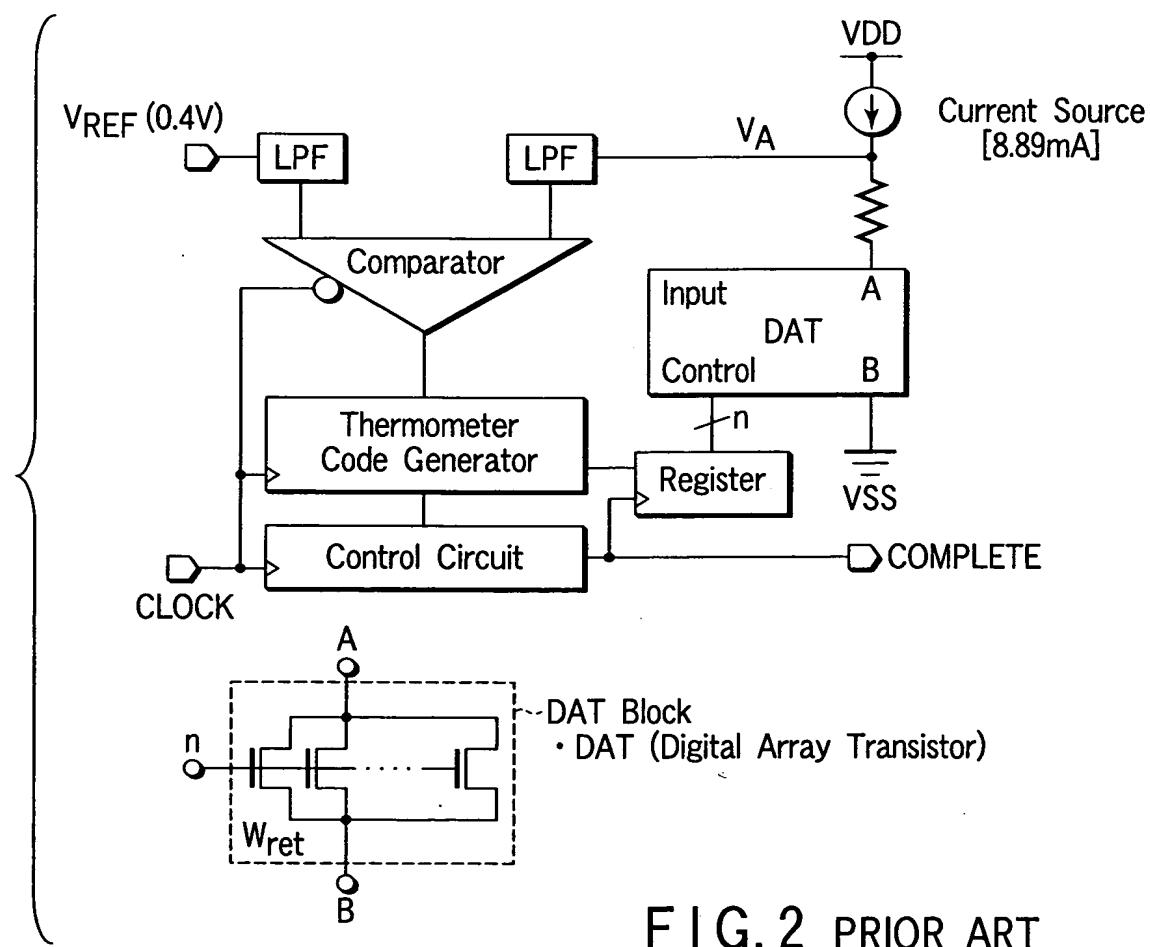


FIG. 2 PRIOR ART

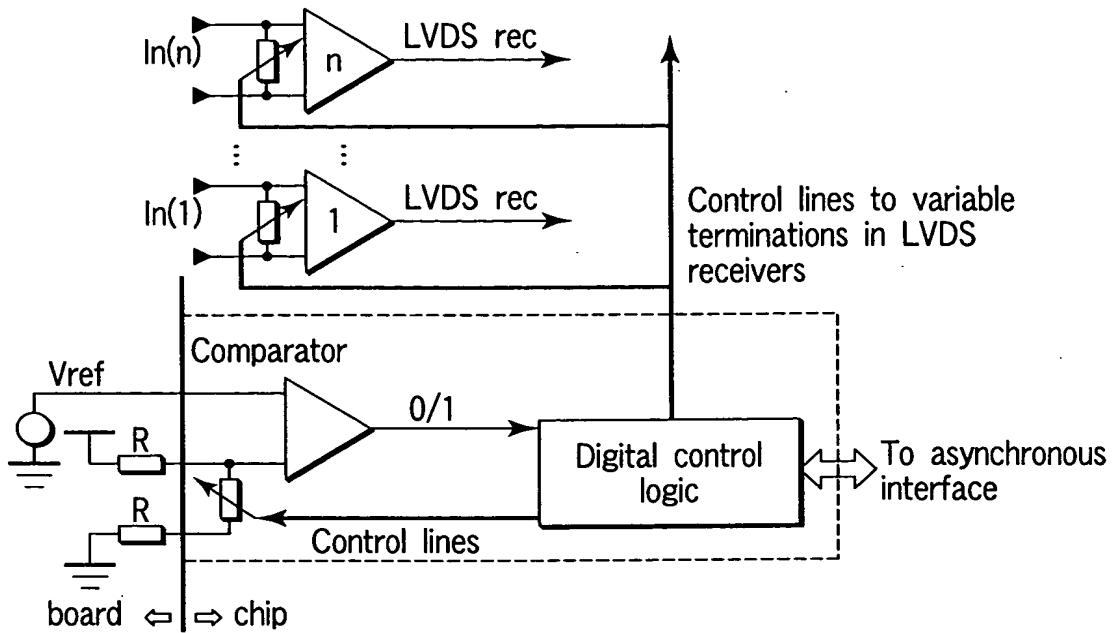


FIG. 3 PRIOR ART

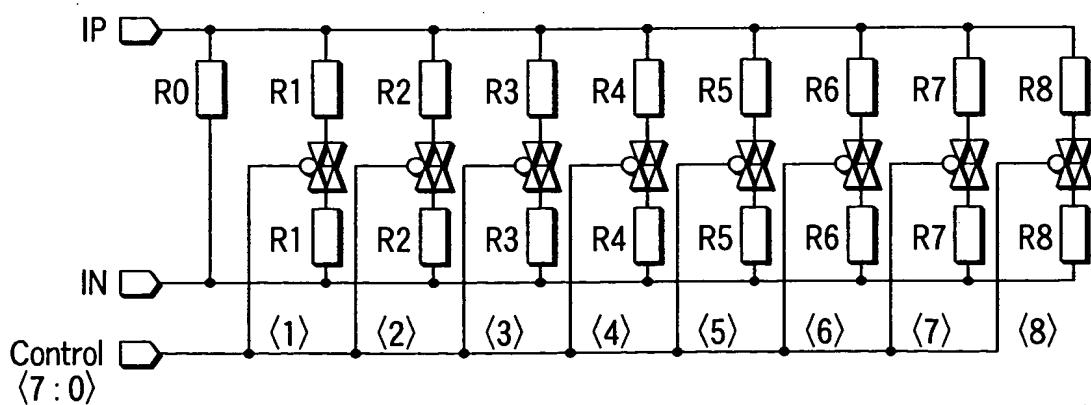


FIG. 4 PRIOR ART

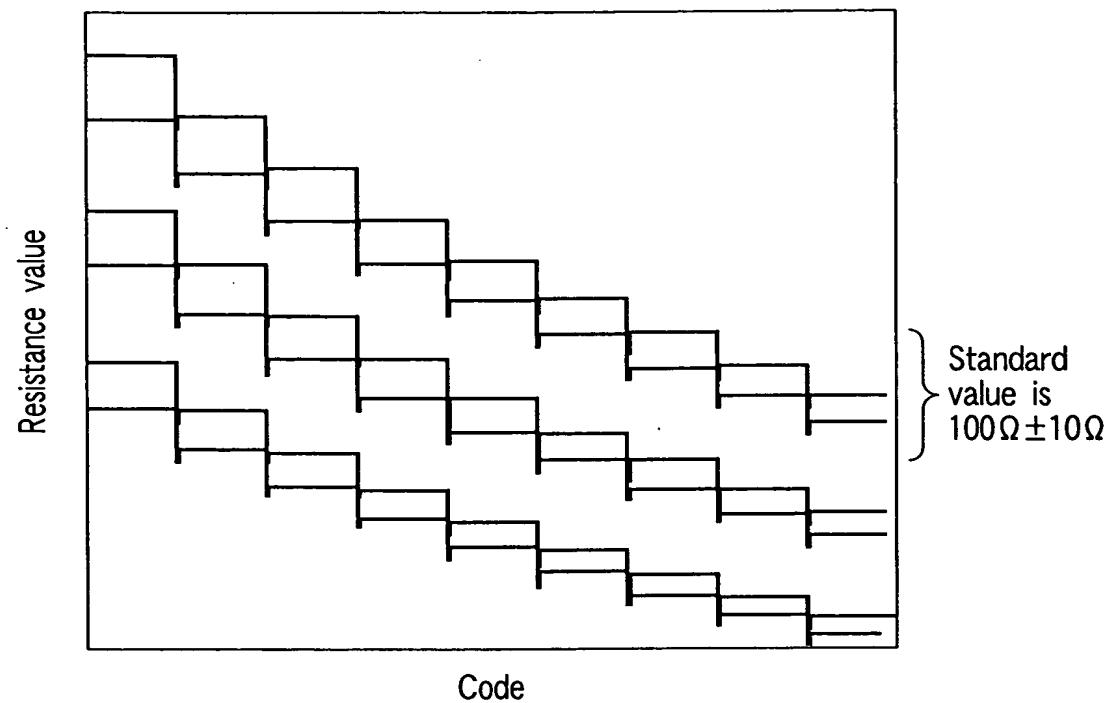


FIG. 5 PRIOR ART

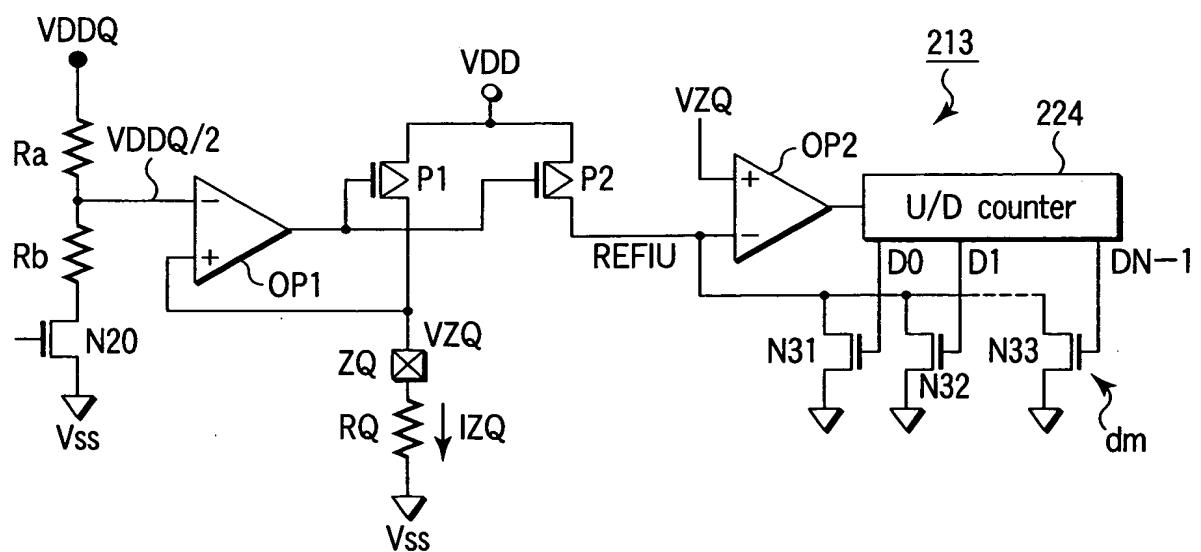


FIG. 6 PRIOR ART

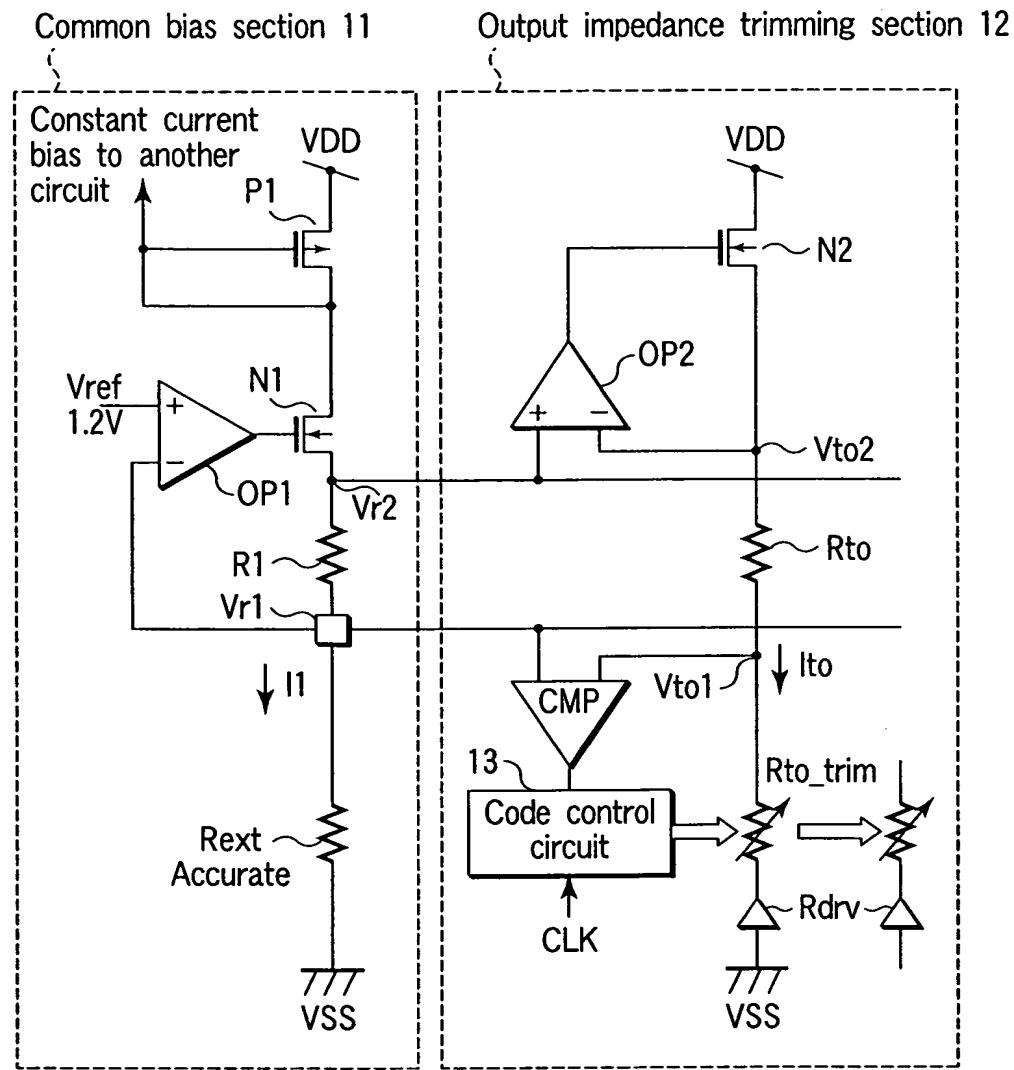


FIG. 7

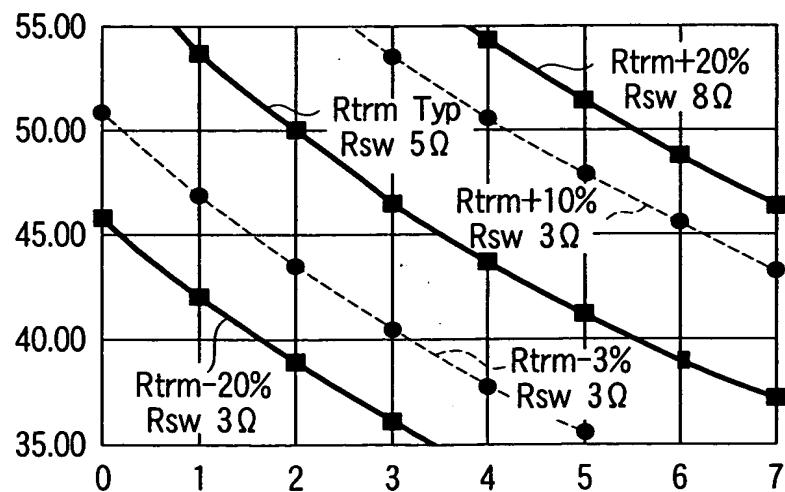


FIG. 8

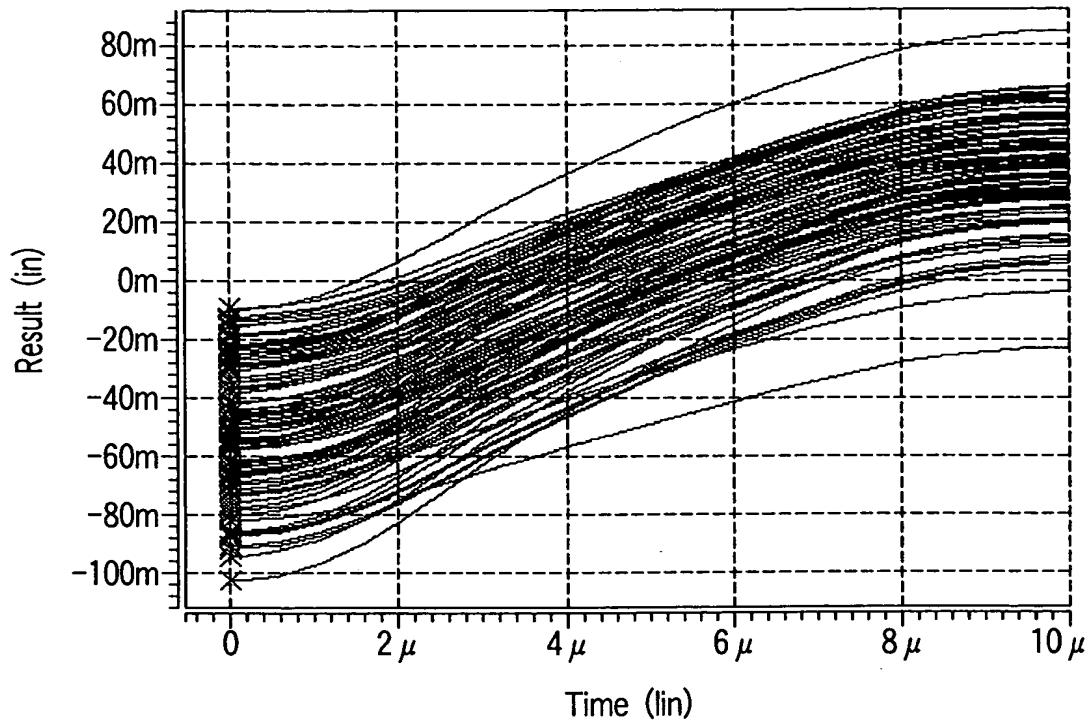


FIG. 9

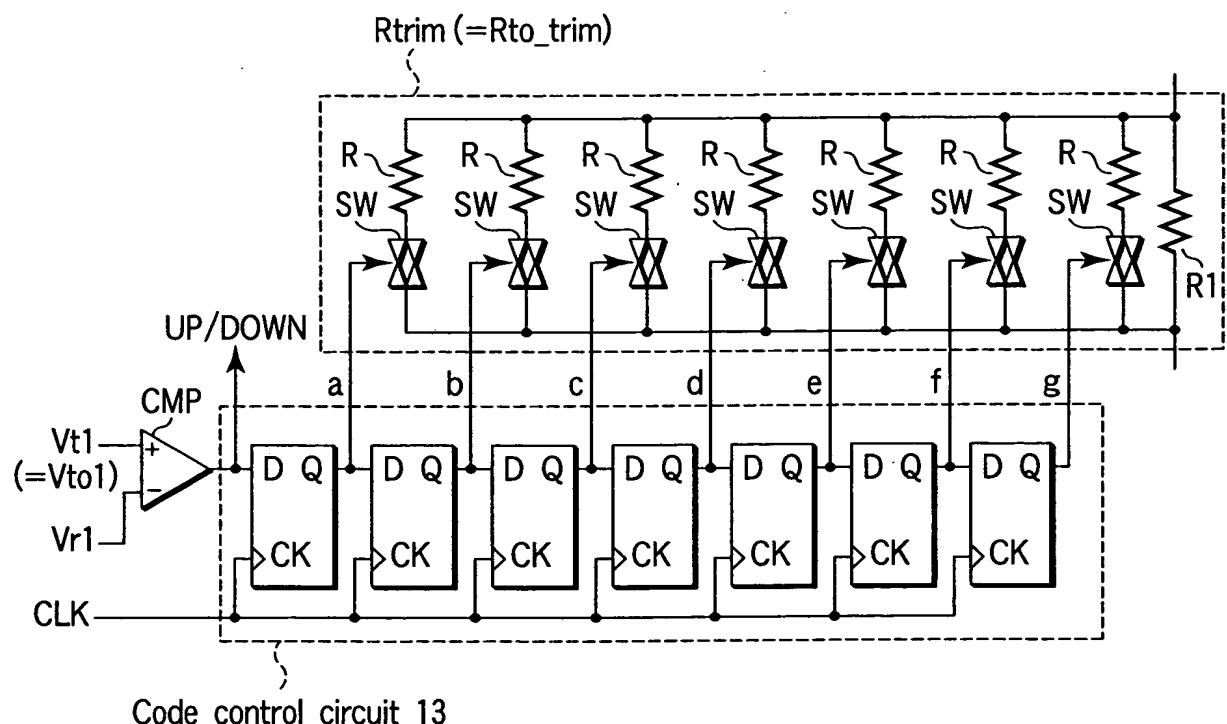
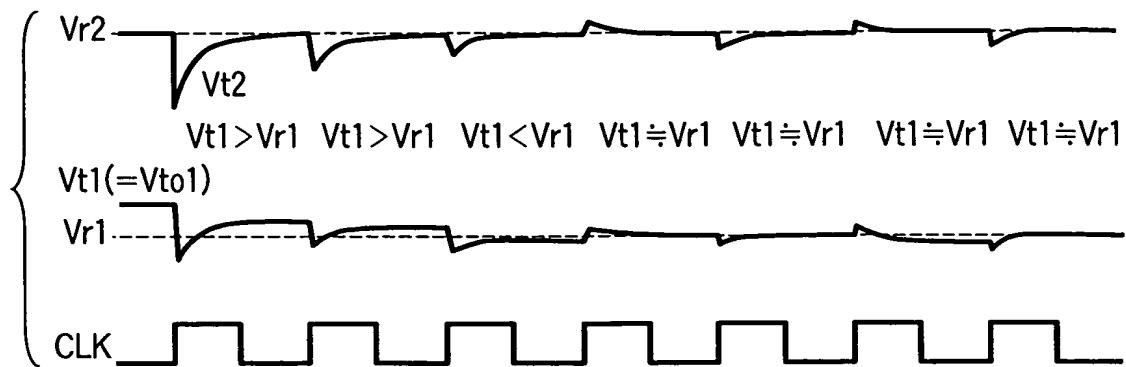
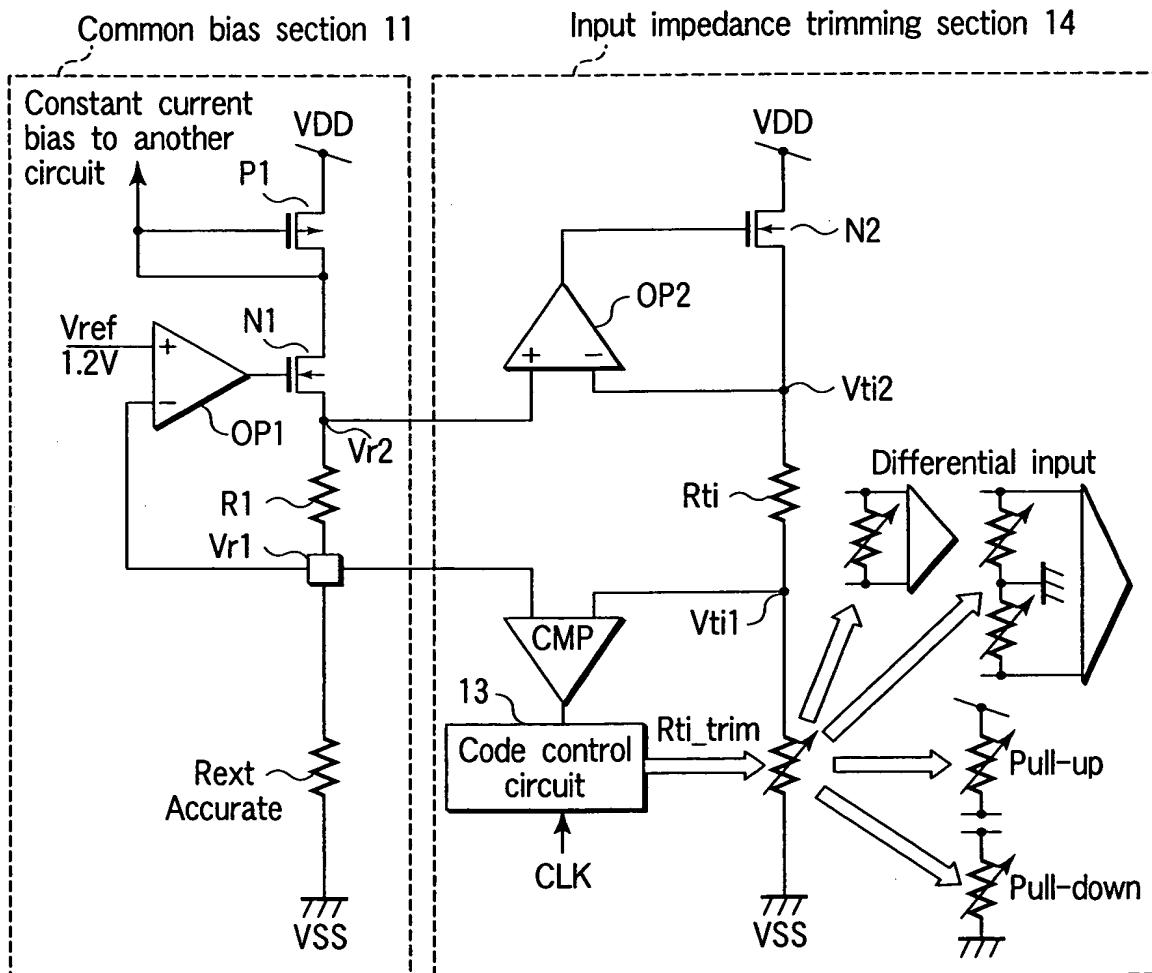


FIG. 10



F I G. 11



F I G. 12

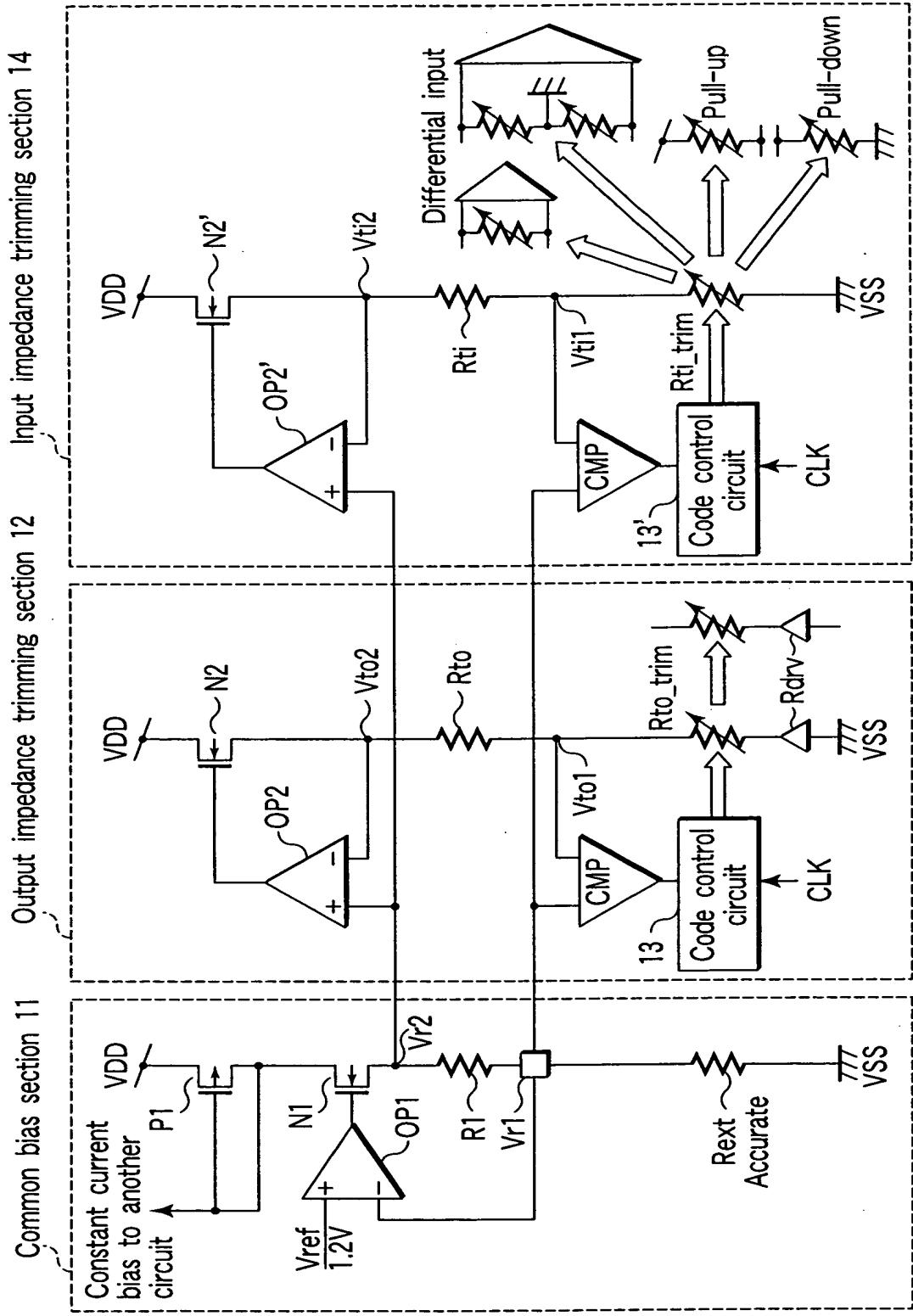
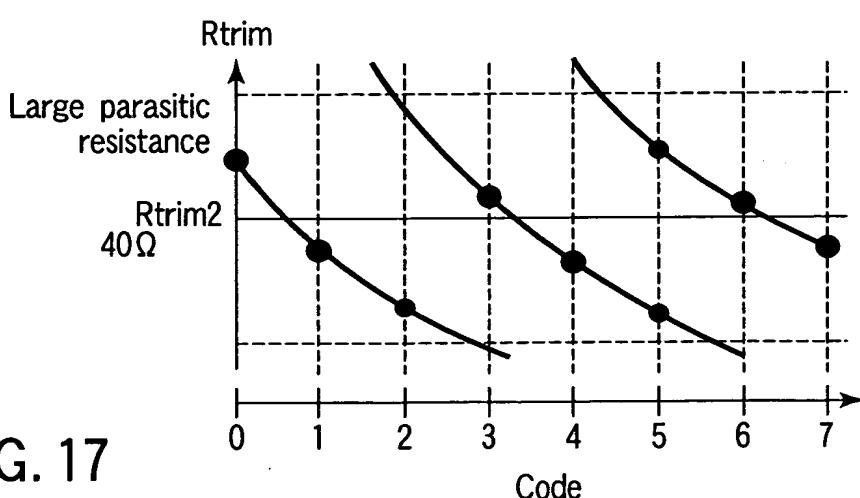
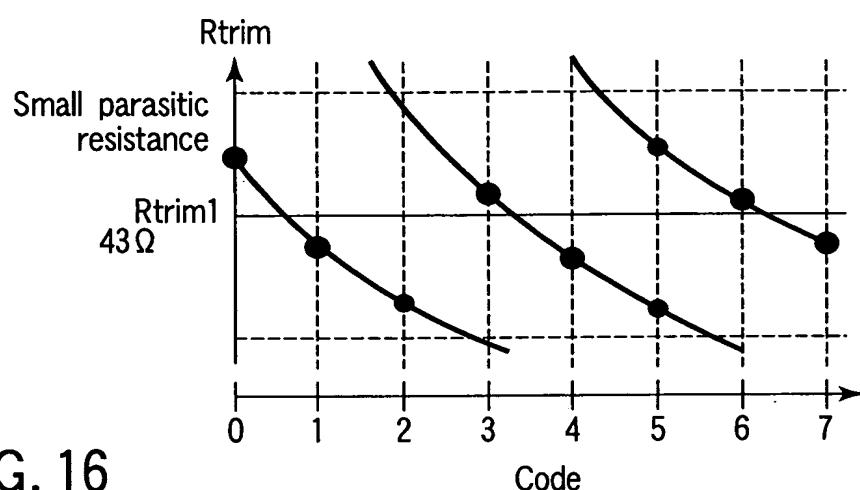
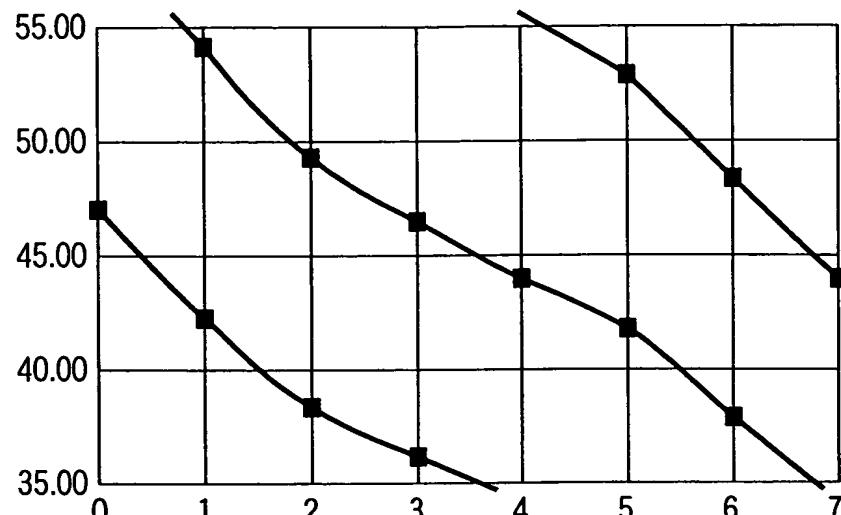


FIG. 13



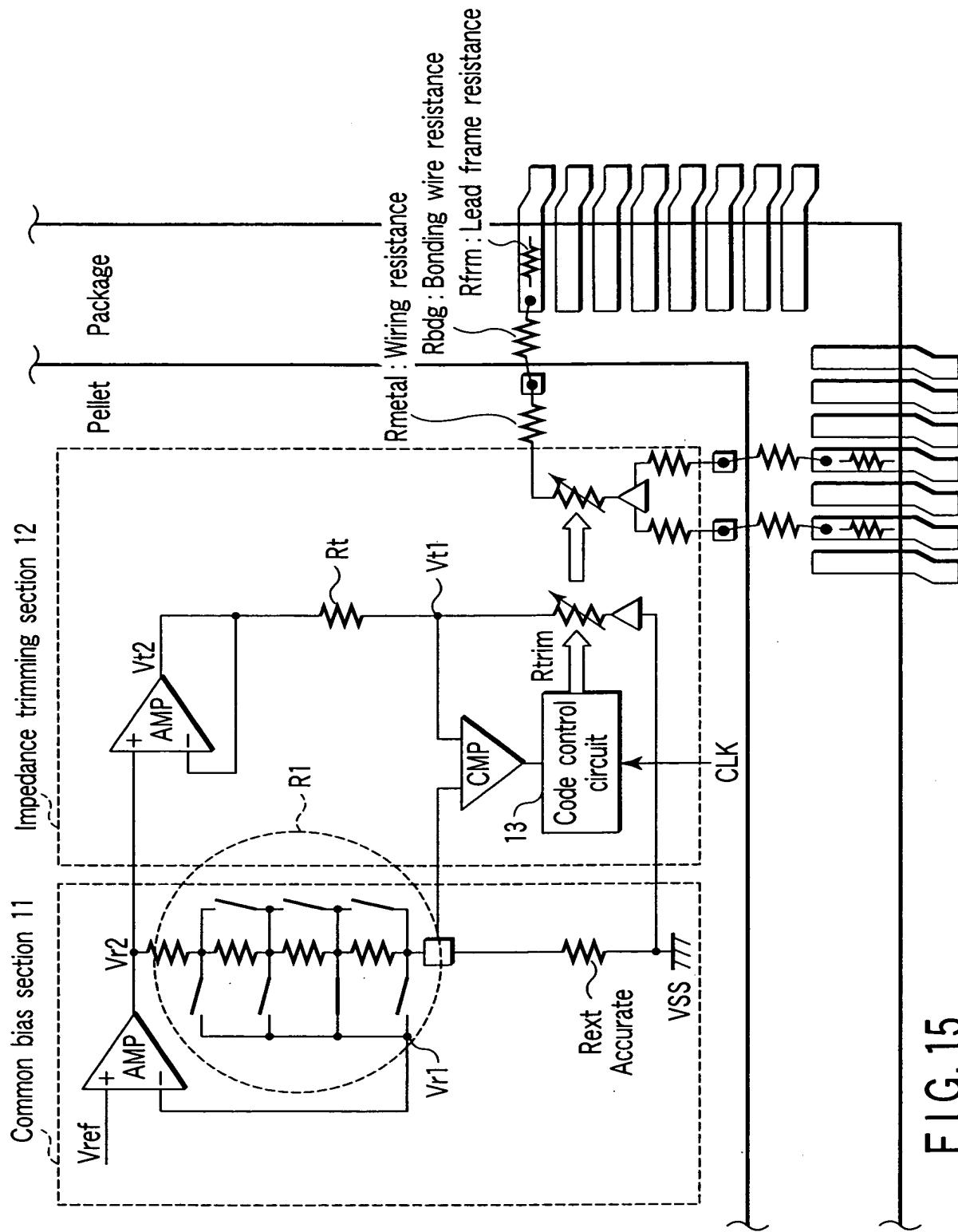
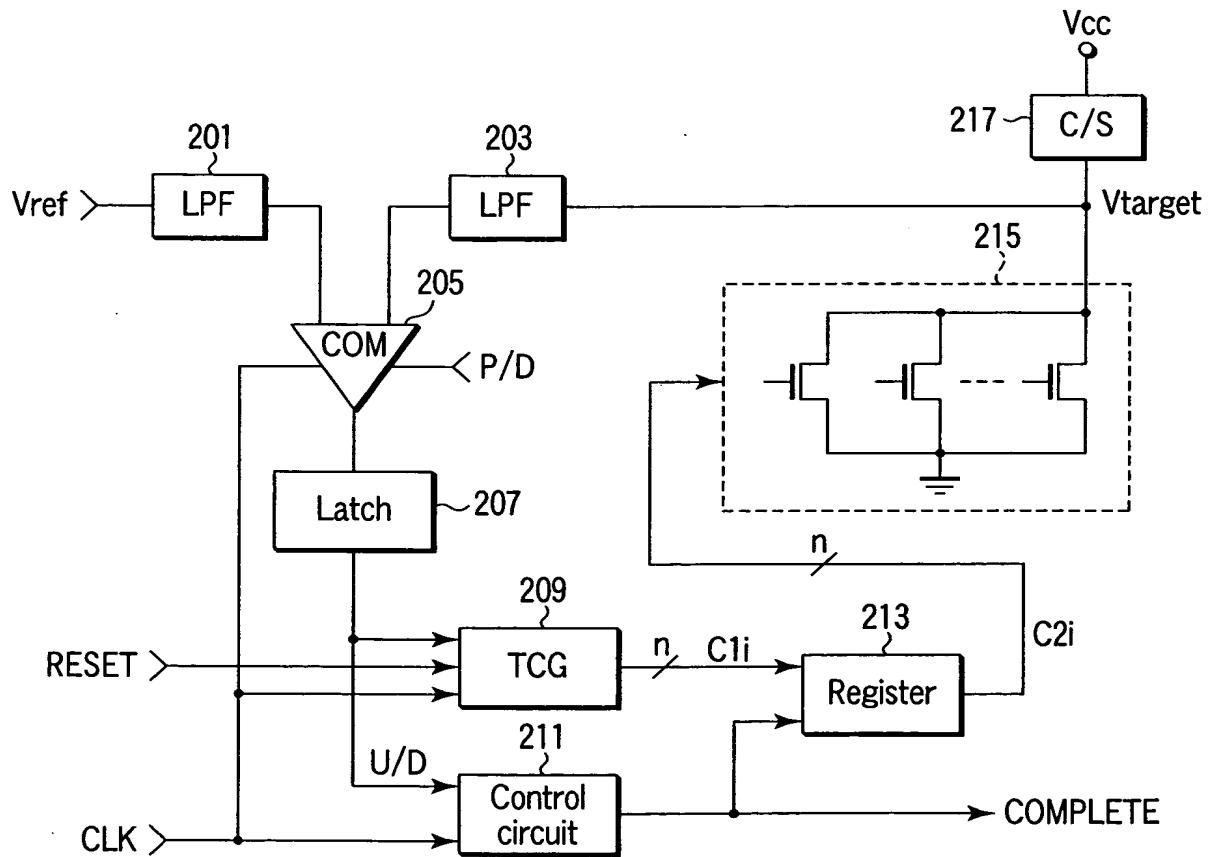
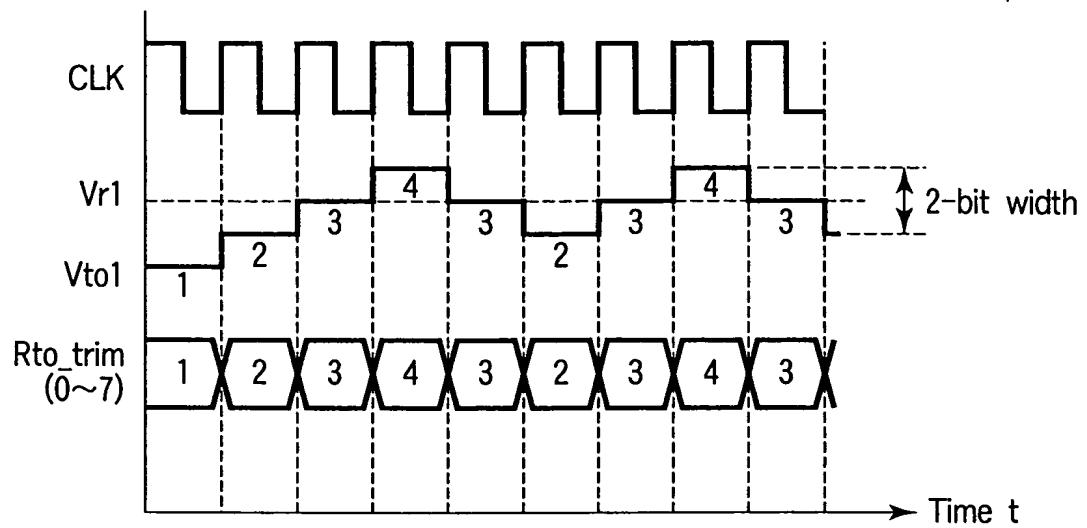
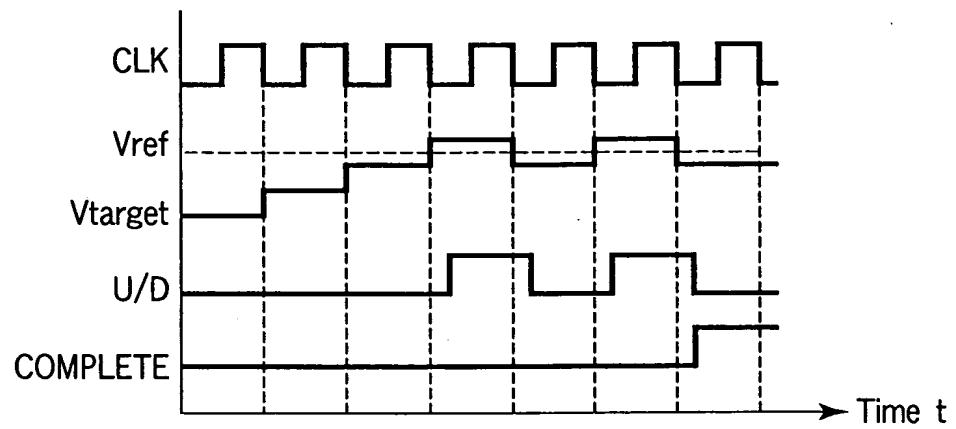
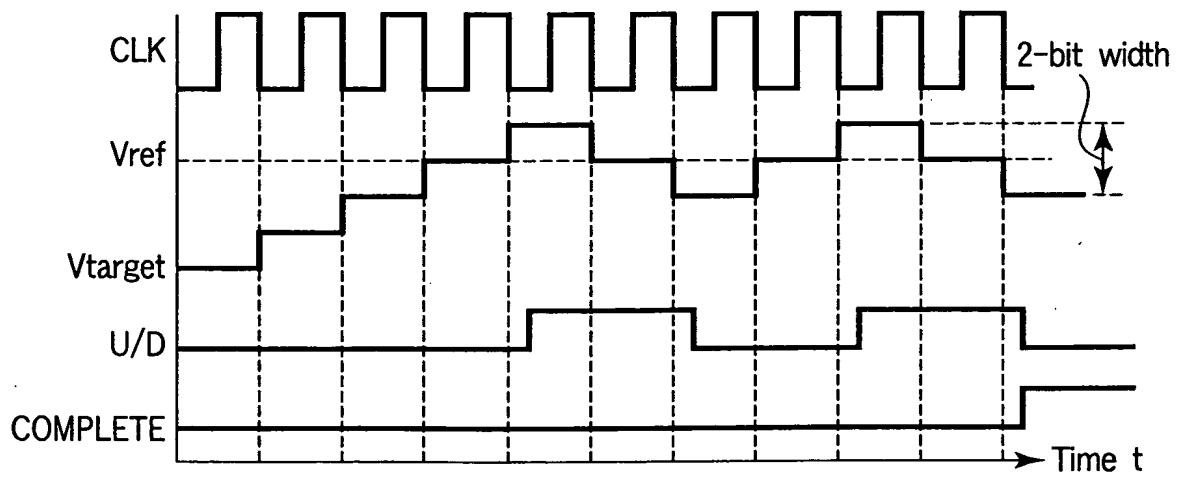


FIG. 15





F I G. 20



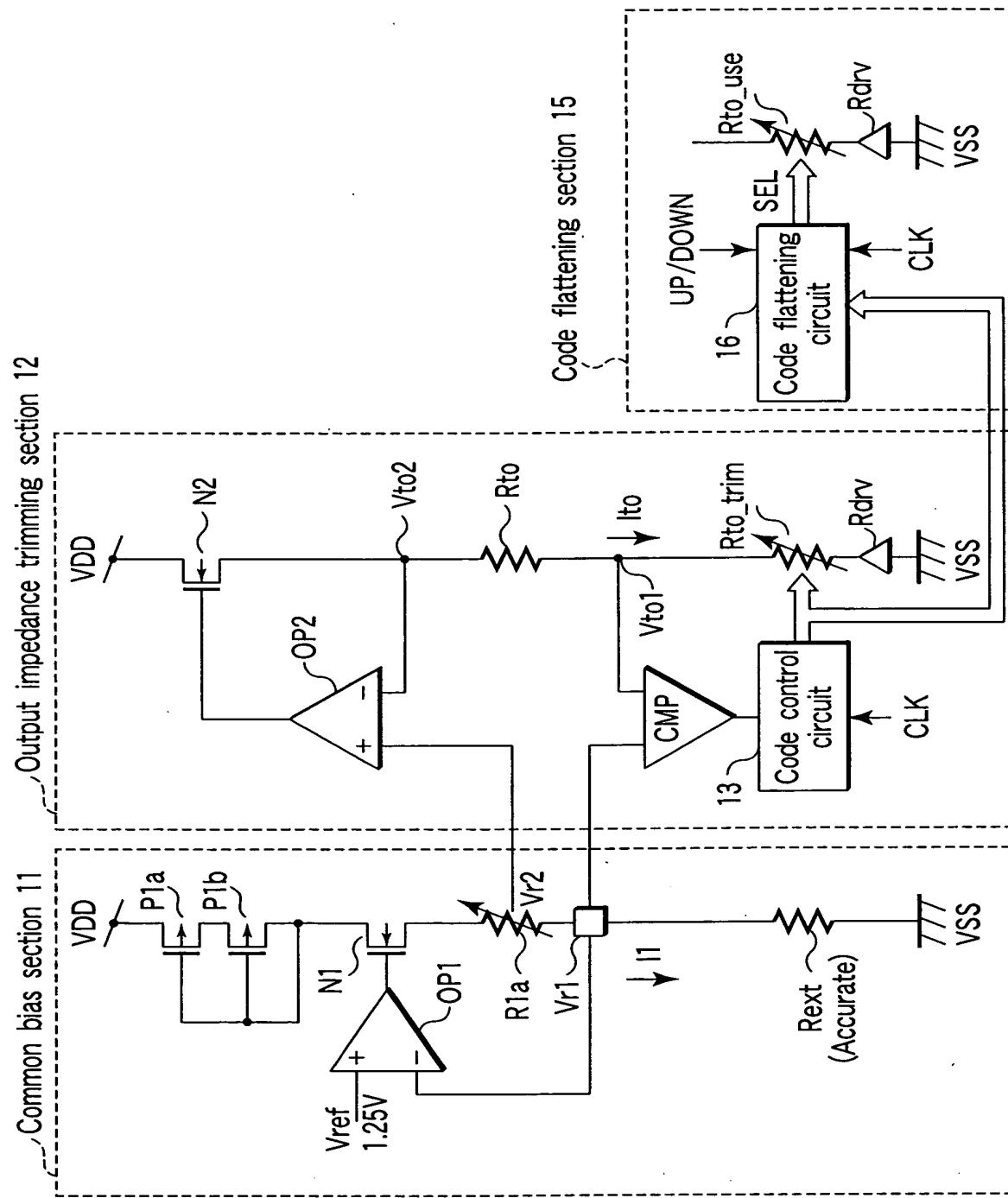


FIG. 22

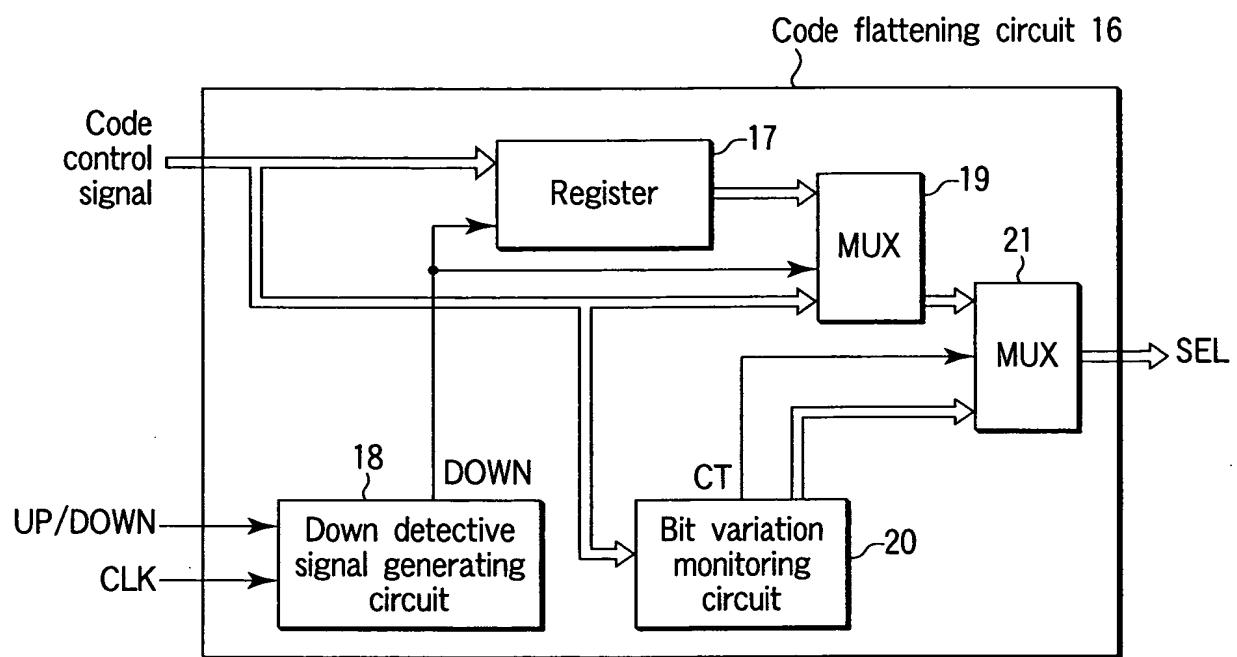


FIG. 23

When V_{to1} varies by 1-bit width

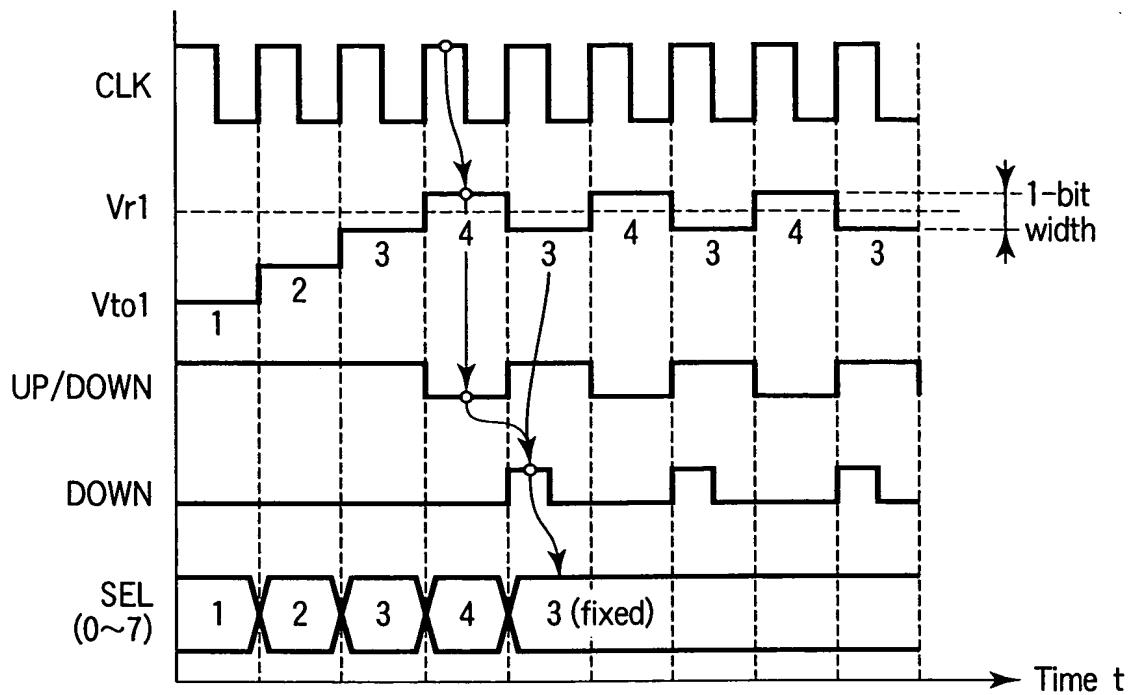


FIG. 24

When V_{to1} varies by 2-bit width

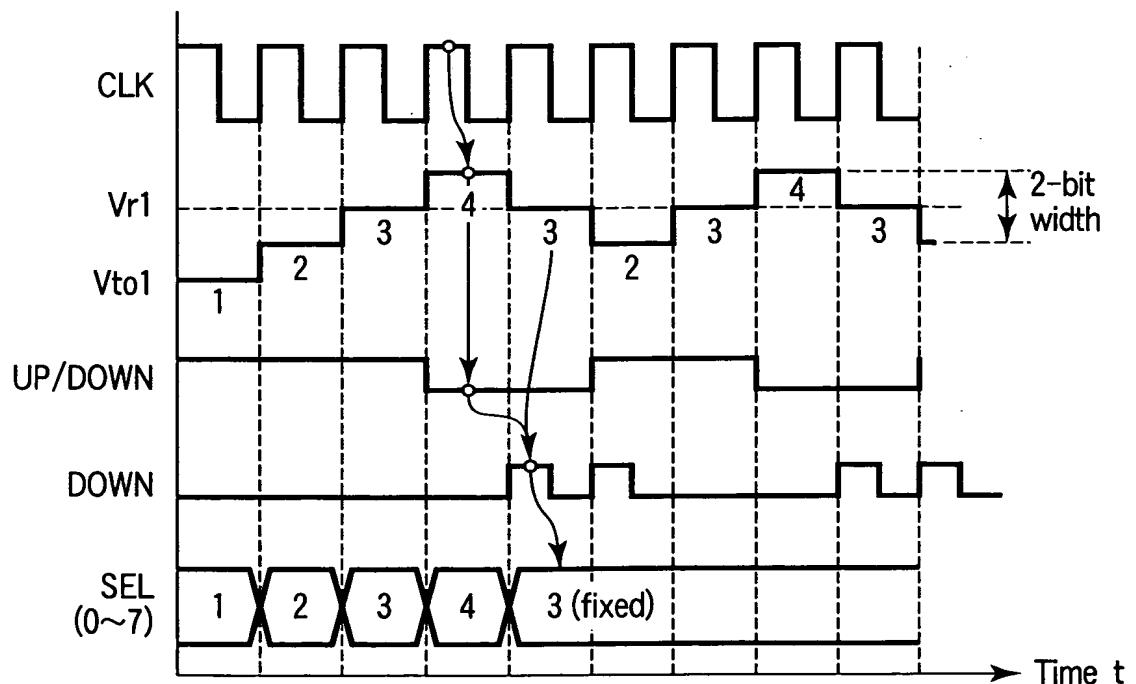


FIG. 25

When V_{to1} varies by 1-bit width

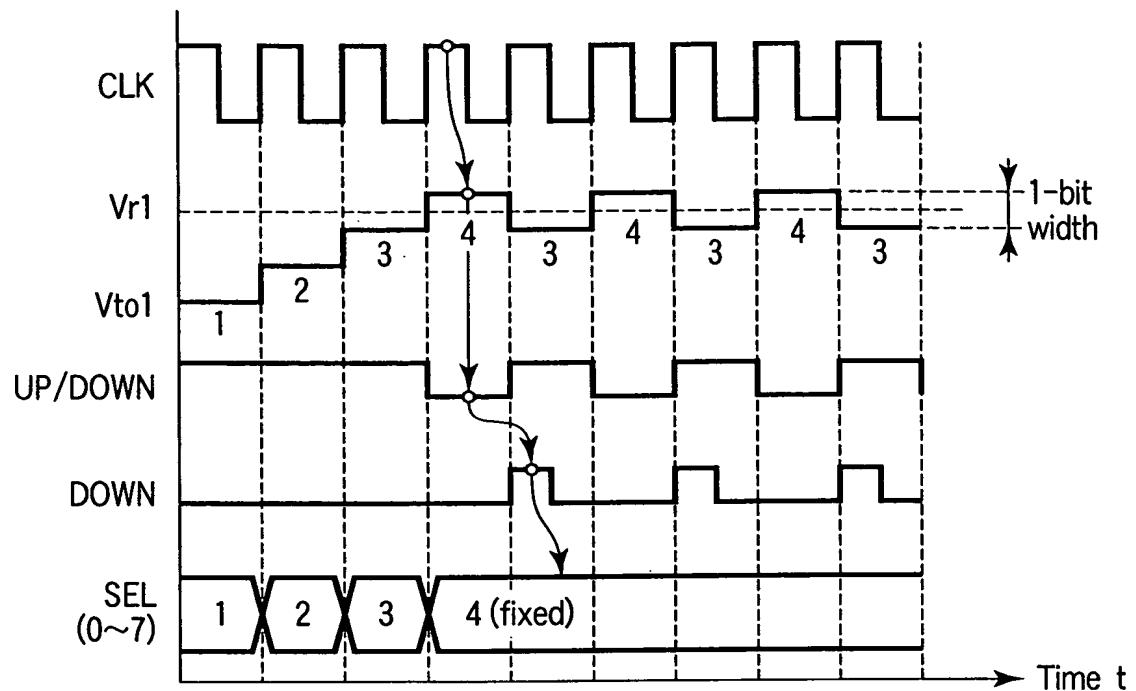


FIG. 26

When V_{to1} varies by 1-bit width

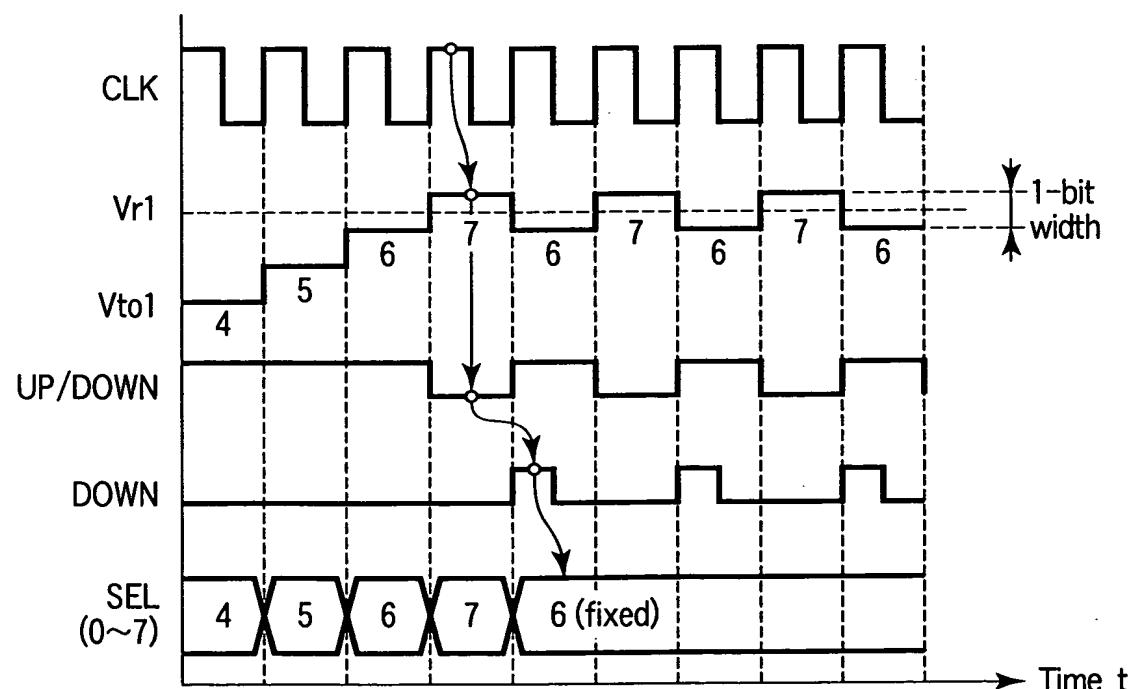


FIG. 27

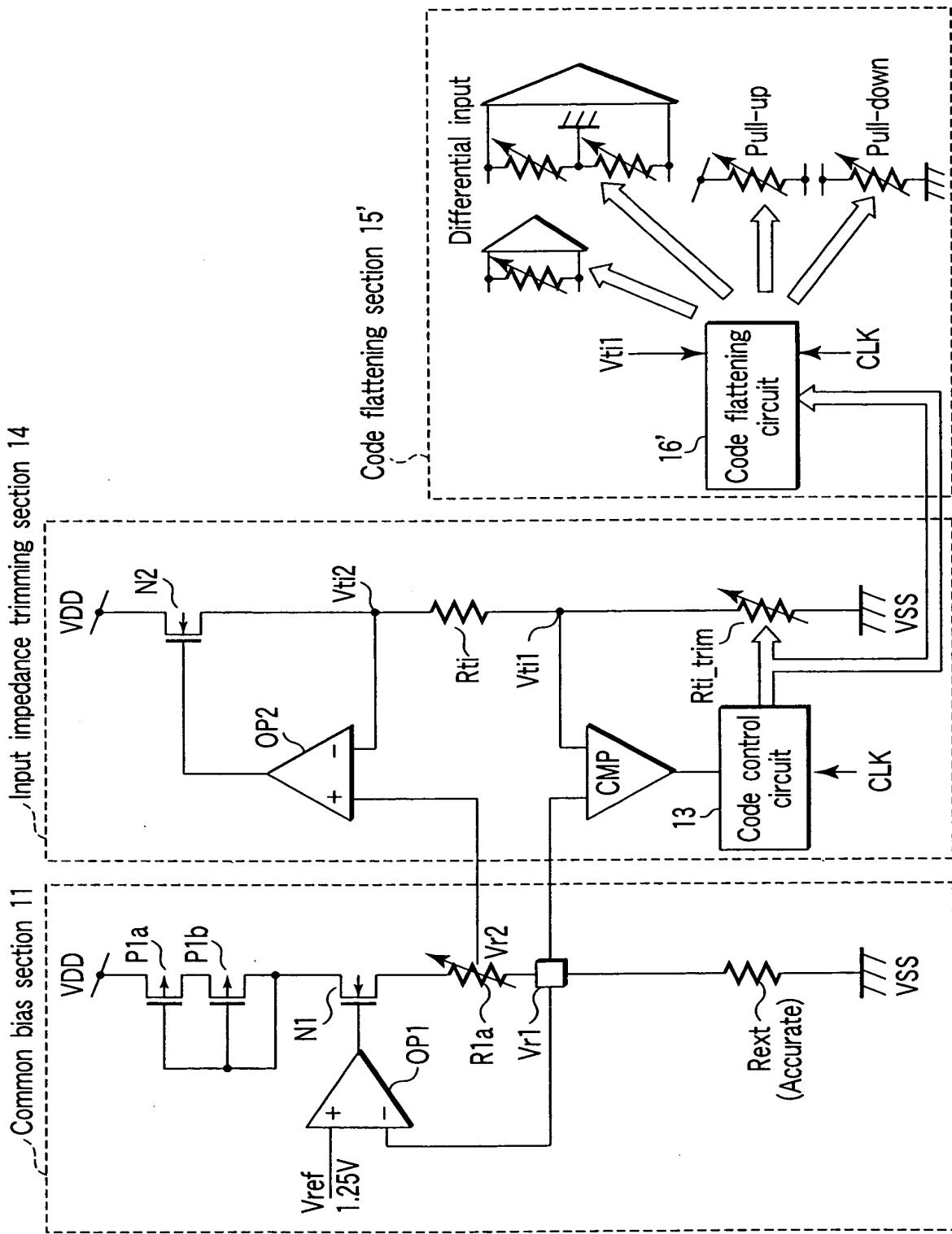


FIG. 28

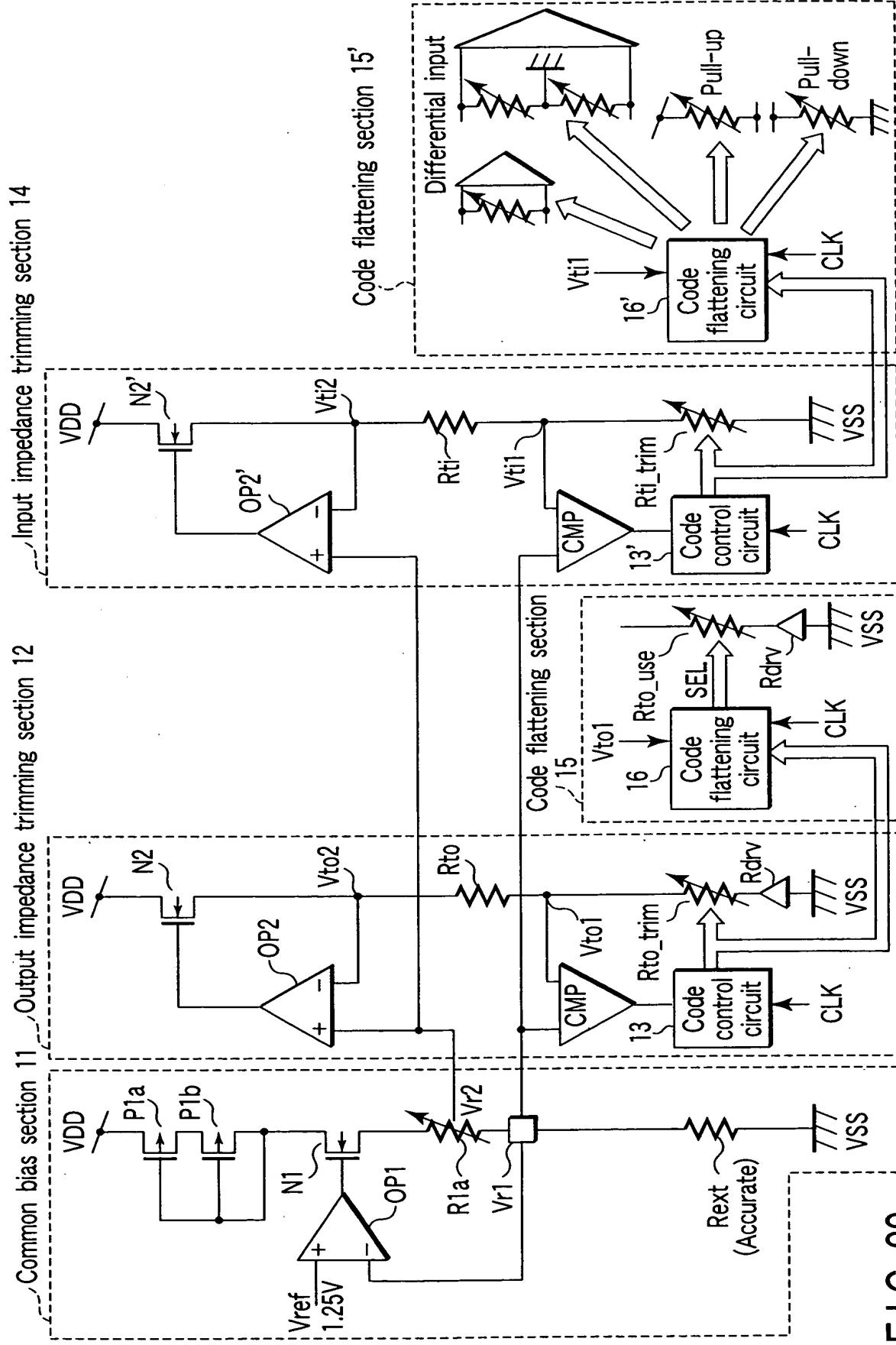


FIG. 29

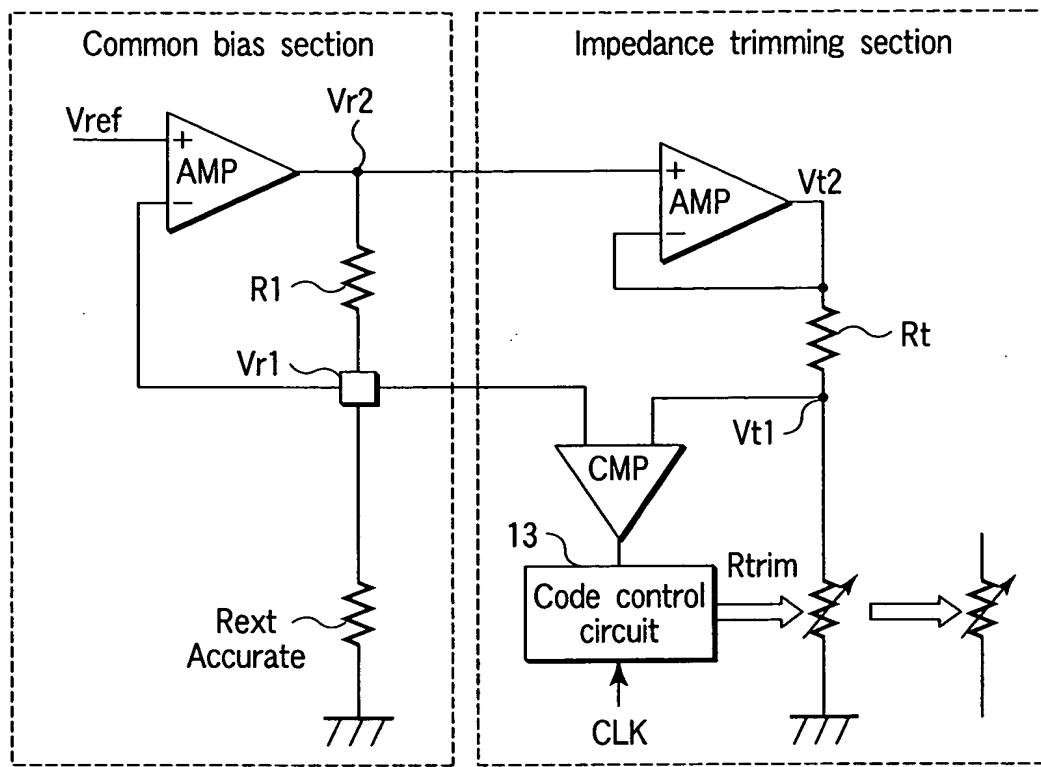


FIG. 30

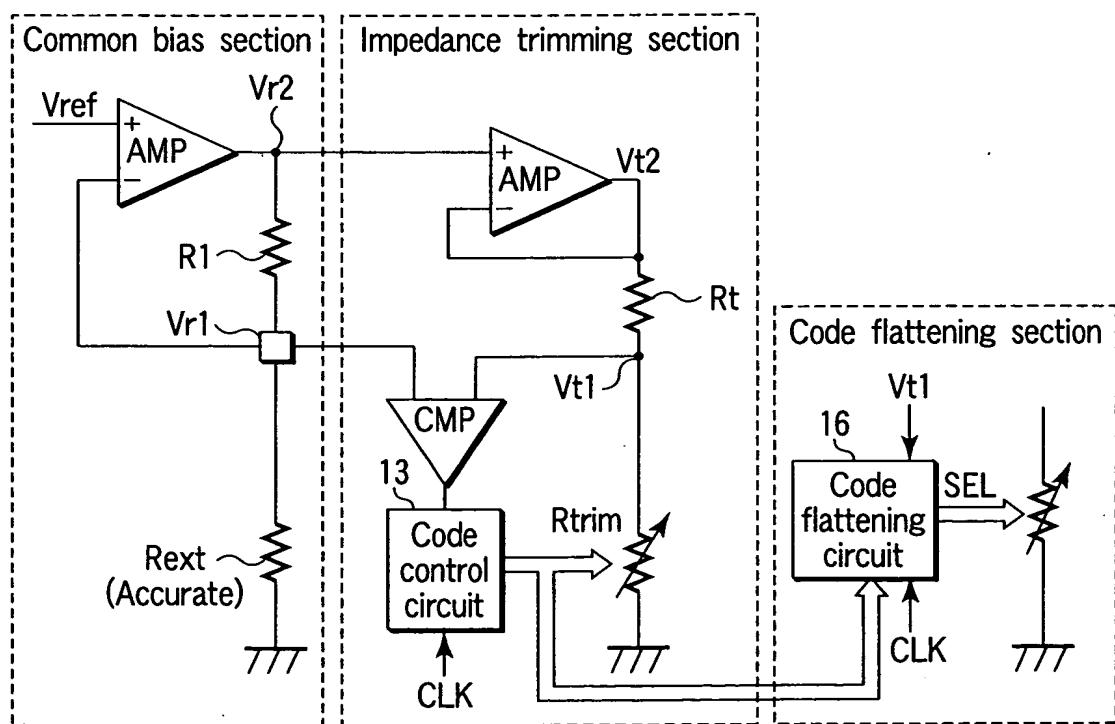


FIG. 31